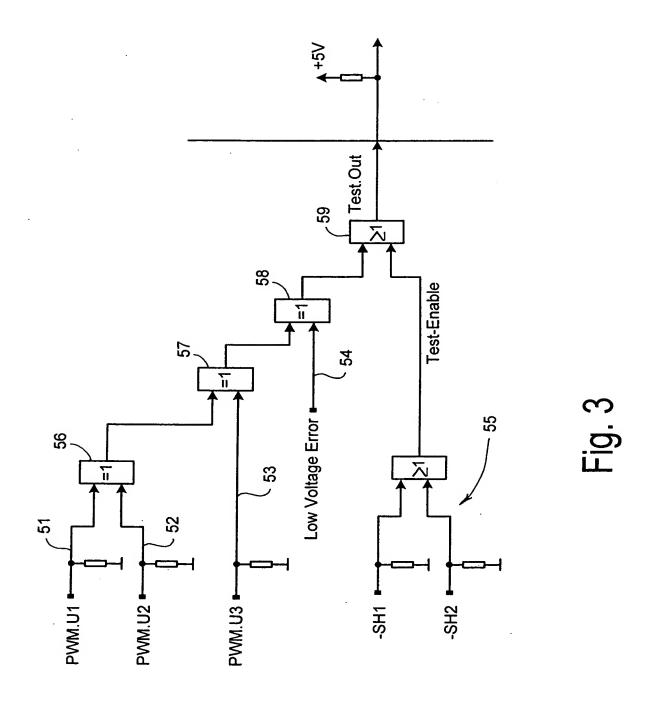


Inventors: Georg Zehentner et al. Serial No. 10/633,275 Attorney Docket No. 56/410



Serial No. 10/633,275

Attorney Docket No. 56/410

Fig. 4

```
PWM - Signals

PWM1: 0 1 0 0 1 0 1 1

PWM2: 0 0 1 0 1 1 0 1

PWM3: 0 0 0 1 1 1 1 0
```

## Test. Out:

## Low Voltages OK

1, 0, 0, 0, 1, 1, 1 // all OK

```
1, 1, 0, 0, 1, 1, 0, 0 // PWM.U1 Stuck on 0
1, 0, 1, 0, 1, 0, 1, 0 // PWM.U2 Stuck on 0
1, 0, 0, 1, 1, 0, 0, 1 // PWM.U3 Stuck on 0

1, 1, 1, 0, 0, 0, 0, 1 // PWM.U1 = PWM.U2
1, 0, 1, 1, 0, 1, 0, 0 // PWM.U2 = PWM.U3
1, 1, 0, 1, 0, 0, 1, 0 // PWM.U1 = PWM.U3
```

1, 0, 0, 0, 0, 0, 0 // PWM.U1 = PWM.U2 = PWM.U3

## Low Voltages Faulty

```
0, 1, 1, 1, 0, 0, 0 // PWM Signals OK
```

```
0, 0, 1, 1, 0, 0, 1, 1 // PWM.U1 Stuck on 0 + low voltage 0, 1, 0, 1, 0, 1, 0, 1 // PWM.U2 Stuck on 0 + low voltage 0, 1, 1, 0, 0, 1, 1, 0 // PWM.U3 Stuck on 0 + low voltage 0, 0, 0, 1, 1, 1, 1, 0 // PWM.U1 = PWM.U2 + low voltage
```

```
0, 1, 0, 0, 1, 1, 1, 0 // PWM.U1 = PWM.U2 + low voltage

0, 1, 0, 0, 1, 0, 1 // PWM.U2 = PWM.U3 + low voltage

0, 0, 1, 0, 1, 1, 0, 1 // PWM.U1 = PWM.U3 + low voltage

0, 1, 1, 1, 1, 1, 1 // PWM.U1 = PWM.U2 = PWM.U3 + low voltage
```

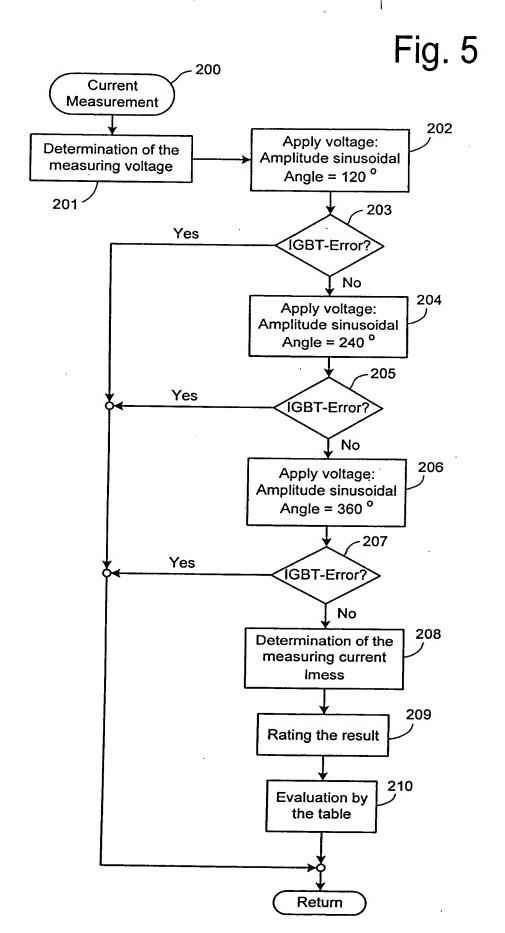
## General errors

```
0, 0, 0, 0, 0, 0, 0, 0 // SH1 or SH2 not "0" 1, 1, 1, 1, 1, 1, 1 // Test – output open
```

Inventors: Georg Zehentner et al.

Serial No. 10/633,275

Attorney Docket No. 56/410



Inventors: Georg Zehentner et al. Serial No. 10/633,275 Attorney Docket No. 56/410

		.,			·		,	
Result				No short circuit	No short circuit Phase 11-12	No short circuit Phase 11-13	No short circuit Phase 12-13	Perform test for Line-to-ground fault
Measurement Step 3	-U/2	-U/2	>	-	-	0	0	0
Measurement Step 2	7/ <b>∩</b> -	n	-U/2	1	0	1	0	0
Measurement Step 1	n	-U/2	-U/2	-	0	0	1	0
·	U1	U2	n3	-ERR				

Inventors: Georg Zehentner et al.

Serial No. 10/633,275

Attorney Docket No. 56/410

